**MCU-to-FPGA Interface System**

**Microcontroller**
- MCU Subroutines
  - Read
  - Write
  - Latch
  - Enable

**FPGA**
- FPGA IP-core
  - Generate Address, Data, & Control

**Microcontroller**
- User MCU Program
  - User program calls built-in subroutines to access FPGA resources

**FPGA**
- User FPGA Design
  - User design connects to address, data, and control signals of the IPcore. Responds to MCU access requests. Uses a minimal protocol.
  - Address Bus (16Mbyte space)
  - Data Bus
  - R/W
  - Request/Acknowledge