External event triggers for software interrupts

Control Computer

Analog Signals

Secondary E-C2 Board

E-C2 System Board

USB Port | Interrupts | A/D Converters | SPI | FPGA LVDS Inputs | FPGA LVDS Outputs
---|---|---|---|---|---
RS-232 Ports |

MCU
Software tasks
User Interfacing
Data handling

FPGA
Logic Functions
Digital Processing
High Speed I/O

Capture Timers

Pulse Width Modulators | MCU 3.3V GPIO
---|---

FPGA 3.3V LV-TTL I/O

12 output channels
12 input channels
2 clock inputs
1 clock output

12 output channels
12 input channels
2 clock inputs
1 clock output

High Speed Serial LVDS

1 clock output

3.3V LV-TTL I/O

30 input or output
2 clock inputs
1 clock output

General Purpose Data I/O

PWM Devices

LCD Displays

Timer Events

Keypads

Switch Panels, Indicators, etc.

Digital Processing

Logic Functions